

UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : 6,865,695 B2  
APPLICATION NO. : 09/915668  
DATED : March 8, 2005  
INVENTOR(S) : Joyner et al.

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Col. 14, (Claim 16), delete lines 4-6, and insert therefore the following:

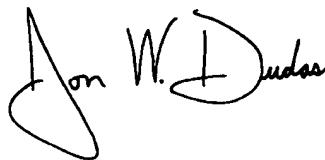
--providing a recovery bus in the high speed system topology  
that operates at a lower speed than the high speed  
interconnect topology;--.

Col. 14, (Claim 17), delete lines 13-16 and insert therefore the following:

--17. The method of claim 16, further including the step of:  
transferring communication from the recovery bus to the  
high speed interconnect topology in response to the  
error being remedied--.

Signed and Sealed this

Eleventh Day of July, 2006

A handwritten signature in black ink, appearing to read "Jon W. Dudas". The signature is stylized with a large, looped initial "J" and a cursive "Dudas".

JON W. DUDAS  
*Director of the United States Patent and Trademark Office*